

Notice of References Cited				Application/Control No. 09/186,388		Applicant(s)/Patent Under Reexamination LEE ET AL.	
				Examiner Ginette Peralta		Art Unit 2814	Page 1 of 1

U.S. PATENT DOCUMENTS								
*		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
							APS	OTHER
<input type="checkbox"/>	A	4,560,879	Dec. 1985	Wu et al.	250	492.2	<input checked="" type="checkbox"/>	<input type="checkbox"/>
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FOREIGN PATENT DOCUMENTS									
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NON-PATENT DOCUMENTS				
*		DOCUMENT (Including Author, Title Date, Source, and Pertinent Pages)	DOCUMENT SOURCE **	
			APS	OTHER
<input type="checkbox"/>	U	S. Wolf, R. Tauber. Silicon Processing for the VLSI Era, Volume 1: Process Technology. Lattice Press, California, 1986. Pages 305-307	<input type="checkbox"/>	<input checked="" type="checkbox"/>
<input type="checkbox"/>	V	S. Wolf. Silicon Processing for the VLSI Era, Volume 2: Process Integration. Lattice Press, California, 1990. Pages 332-334	<input type="checkbox"/>	<input checked="" type="checkbox"/>
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*A copy of this reference is not being furnished with this Office action. (See Manual of Patent Examining Procedure, Section 707.05(a).)

**APS encompasses any electronic search i.e. text, image, and Commercial Databases.

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